

FORM PTO-1449	Express Mail Number EV3#247058/LLS	Docket Number (Opti nal) 100-14320 (P04927-D01)	Application N . N w (Div. f 09/824,653)
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		Applicant(s) Terry Lines	
<i>(Use several sheets if necessary)</i>		Filing Date Her with	Gr up Art Unit Unkn wn

U.S. PATENT DOCUMENTS												
EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
WLL	5	2	1	0	4	3	7	05/11/93	Sawada et al.	257	392	08/10/92
WLL	6	1	9	8	1	4	0	03/06/01	Muramoto et al.	257	392	09/08/99
WLL	5	5	8	1	1	0	7	12/03/96	Kawamura et al.	257	392	12/14/94
WLL	5	0	1	6	0	7	7	05/14/91	Sato et al.	357	41	08/22/86
WLL	6	4	7	6	4	3	0	11/05/02	Schmitz et al.	257	274	09/07/00
WLL	4	4	7	2	8	7	1	09/25/84	Green et al.	29	571	11/19/80
WLL	6	0	2	7	9	7	8	02/22/00	Gardner et al.	438	289	01/28/97
WLL	4	3	2	9	1	8	6	05/11/82	Kotecha et al.	148	1.5	12/20/79
WLL	6	0	2	0	2	2	7	02/01/00	Bulucea	438	194	03/07/97
WLL	5	2	4	5	2	0	7	09/14/93	Mikoshiha et al.	257	392	08/17/90
WLL	6	0	4	0	6	1	0	03/21/00	Noguchi et al.	257	392	04/08/98
WLL	4	2	0	5	3	3	0	05/27/80	Klein	357	23	04/17/78
WLL	4	5	7	8	6	9	4	03/25/86	Ariizumi et al.	357	41	09/30/82

FOREIGN PATENT DOCUMENTS												
DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	Translation	
											Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
WLL	Richard S. Muller and Theodore I. Kamins, "Device Electronics for Integrated Circuits", Second Edition, pps. 477-479.

EXAMINER <i>WLL</i>	DATE CONSIDERED 13/4/06
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	